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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLAIMS	IND CLAIMS
10/574,641	12/14/2006	2825	1390	09432.0065	18	1

**CONFIRMATION NO. 2211**

**CORRECTED FILING RECEIPT**

60668  
SAP / FINNEGAN, HENDERSON LLP  
901 NEW YORK AVENUE, NW  
WASHINGTON, DC20001-4413

Date Mailed: 08/01/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

**Applicant(s)**

Daniel Scheibli, Palo Alto, CA;

**Power of Attorney:** The patent practitioners associated with Customer Number 60668

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/EP04/52044 09/06/2004

**Foreign Applications**

GERMANY 103 46 303.8 10/06/2003

**If Required, Foreign Filing License Granted:** 05/12/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is  
**US10/574,641**

**Projected Publication Date:** 08/23/2007

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Processing of successive questions of an external computer in a computerised system  
comprising at least a first computer and a second computer

**Preliminary Class**

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